08-23-05
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7590

05/27/2005

John S. Beulick
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(Signature)

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/764,243	01/23/2004	Zoran Stanic	143461	8033

TITLE OF INVENTION: METHODS AND APPARATUS FOR ANALYZING HIGH VOLTAGE CIRCUIT BREAKERS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	08/29/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
BENSON, WALTER	2858	324-424000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Armstrong Teasdale LLP

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(A) NAME OF ASSIGNEE

General Electric Company

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Schenectady, New York

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature _____

Date

August 22, 2005

Typed or printed name

Robert B. Reeser, III

Registration No.

45,548

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PATENT
143461

IN THE UNITED STATES OFFICE OF PATENTS AND TRADEMARKS

Applicant: Zoran Stanisic
Serial No.: 10/764,243
Filed: January 23, 2004
For: METHODS AND
APPARATUS FOR
ANALYZING HIGH
VOLTAGE CIRCUIT
BREAKERS

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: Group No.: 2858
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: Examiner: Benson, Walter
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:
:

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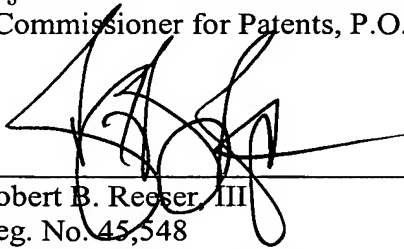
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